

Prior to Examination, please amend the above-identified application as follows:

AMENDMENTS

In The Claims

Please add the following new claims:

31
1 34. (Newly Added) A digital subscriber line (DSL) communication device,
2 comprising:
3 a receiver means for developing a received signal;
4 a digital signal processor (DSP) means configured to perform layer two
5 error detection by computing a frame check sequence (FCS) on each frame of said
6 received signal; and
7 means for saving the adaptive parameters of an adaptive device located
8 within said receiver means, and calculated by said DSP means, if said frame check
9 sequence indicates that said received signal is error free.

1 35. (Newly Added) The apparatus as defined in claim 34, wherein said DSL
2 device operates in a multipoint environment.

1 36. (Newly Added) The apparatus as defined in claim 34, wherein said DSL
2 device operates in a half duplex environment.

1 37. (Newly Added) The apparatus as defined in claim 34, wherein said DSL
2 device operates in a full duplex environment.

1 38. (Newly Added) The apparatus as defined in claim 34, wherein said DSL
2 device operates in an asymmetrical duplex environment.

1 39. (Newly Added) The apparatus as defined in claim 34, wherein said means
2 for saving the adaptive parameters of an adaptive device located within said receiver
3 means resides in layer one of the OSI seven layer model.

1 40. (Newly Added) The apparatus as defined in claim 34, wherein said frame
2 check sequence is used to calculate the adaptive parameters of a device chosen from the
3 group consisting of an equalizer, echo-canceller, adapted gain device, and timing loop.

1 41. (Newly Added) The apparatus as defined in claim 34, wherein said frame
2 check sequence is used to adapt a receive margin level based on said received signal.

1 42. (Newly Added) A digital subscriber line (DSL) communication device,
2 comprising:

3 a receiver means for developing a received signal;

4 a digital signal processor (DSP) means configured to perform layer two
5 error detection by computing a frame check sequence (FCS) on each frame of said
6 received signal; and

7 means for using existing parameters of an adaptive device located within
8 said receiver means if said frame check sequence indicates that said received signal
9 contains errors.

1 43. (Newly Added) The apparatus as defined in claim 42, wherein said DSL
2 device operates in a multipoint environment.

1 44. (Newly Added) The apparatus as defined in claim 42, wherein said DSL
2 device operates in a half duplex environment.

1 45. (Newly Added) The apparatus as defined in claim 42, wherein said DSL
2 device operates in a full duplex environment.

1 46. (Newly Added) The apparatus as defined in claim 42, wherein said DSL
2 device operates in an asymmetrical duplex environment.

1 47. (Newly Added) A method for updating adaptive parameters in a digital
2 subscriber line (DSL) communication device, comprising the steps of:
3 developing, in a receiver, a received signal;
4 performing, in a digital signal processor (DSP), layer two error detection
5 by computing a frame check sequence (FCS) on each frame of said received signal; and
6 saving the adaptive parameters of an adaptive device located within said
7 receiver, and calculated by said DSP, if said frame check sequence indicates that said
8 received signal is error free.

1 48. (Newly Added) The method as defined in claim 47, wherein said DSL
2 device operates in a multipoint environment.

1 49. (Newly Added) The method as defined in claim 47, wherein said DSL
2 device operates in a half duplex environment.

1 50. (Newly Added) The method as defined in claim 47, wherein said DSL
2 device operates in a full duplex environment.

1 51. (Newly Added) The method as defined in claim 47, wherein said DSL
2 device operates in an asymmetrical duplex environment.

1 52. (Newly Added) The method as defined in claim 47, wherein said step of
2 saving the adaptive parameters of an adaptive device located within said receiver occurs
3 in layer one of the OSI seven layer model.

1 53. (Newly Added) The method as defined in claim 47, further comprising
2 the step of using said frame check sequence to calculate the adaptive parameters of a
3 device chosen from the group consisting of an equalizer, an echo-canceller, an adaptive
4 gain device, and a timing loop.

1 54. (Newly Added) The method as defined in claim 47, wherein said frame
2 check sequence is used to adapt a receive margin level based on said received signal.

1 55. (Newly Added) A method for updating adaptive parameters in a digital
2 subscriber line (DSL) communication device, comprising the steps of:

3 developing, in a receiver, a received signal;
4 performing, in a digital signal processor (DSP), layer two error detection
5 by computing a frame check sequence (FCS) on each frame of said received signal; and
6 using existing parameters of an adaptive device located within said
7 receiver if said frame check sequence indicates that said received signal contains errors.

1 56. (Newly Added) The method as defined in claim 55, wherein said DSL
2 device operates in a multipoint environment.

1 57. (Newly Added) The method as defined in claim 55, wherein said DSL
2 device operates in a half duplex environment.

1 58. (Newly Added) The method as defined in claim 55, wherein said DSL
2 device operates in a full duplex environment.

1 59. (Newly Added) The method as defined in claim 55, wherein said DSL
2 device operates in an asymmetrical duplex environment.

CONCLUSION

Applicant respectfully requests that the pending claims of this application be allowed to issue. If the Examiner has any comments regarding Applicant's response or intends to dispose of this matter in a manner other than a notice of allowance, Applicant requests that the Examiner telephone Applicant's undersigned attorney.

**Thomas, Kayden, Horstemeyer
& Risley**
100 Galleria Parkway, N.W.
Suite 1750
Atlanta, Georgia 30339-5948
(770) 933-9500

Respectfully Submitted,

A handwritten signature in black ink, appearing to read "Michael J. Tempel", written over a horizontal line.

Michael J. Tempel
Registration No. 41,344